Docket No. 271227US41X PCT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Samuel BOUTIN, et al.

SERIAL NO: 10/532,080 ART UNIT: 3628

FILING DATE: November 10, 2005 EXAMINER: Geroge Y. C. CHEN

FOR: METHOD AND DEVICE FOR SYNTHESISING AN ELECTRICAL ARCHITECTURE

FEE TRANSMITTAL

No additional fee is required

☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.

The Fee has been calculated as shown below:

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	18 - 20 =	0	x \$52 =	\$0.00
INDEPENDENT CLAIMS	3 - 3 =	0	x \$220 =	\$0.00
☐ MULTIPLE DEPENDENT CLAIMS (If applicable) + \$390 =			\$0.00	
☐ LATE FILING OF DECL	ARATION		+ \$130 =	\$0.00
			BASIC FEES	\$0.00
TOTAL OF ABOVE CALCULATIONS				\$0.00
☐ REDUCTION BY 50% FOR FILING BY SMALL ENTITY			\$0.00	
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The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no payment is enclosed herewith, or credit any overpayment to Deposit Account No. <u>15-0030</u>, with the **EXCEPTION** of deficiencies in fees for multiple dependent claims in new applications.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030.